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Sheet	1	of	3
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Application Number	10/766,217
Filing Date	January 27, 2004
First Named Inventor	Guterman
Art Unit	
Examiner Name	
Attorney Docket Number	SAND-01011US0

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

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Date	
Considered	

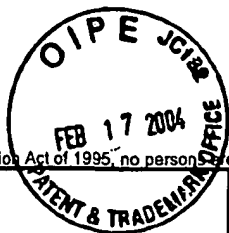
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PTO/SB/088 (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JA		KURATA, Hideaki, et al., Constant-Charge-Injection Programming for 10-MB/s Multilevel AG-AND Flash Memories, 2002 Symposium On VLSI Circuits Digest of Technical Papers, pp. 302-303	
JA		JOHNSON, William S., et al., SESSION XII: ROMs, PROMs and EROMs, 1980 IEEE International Solid State Circuits Conference, pp. 152-153	
JA		NOBUKATA, Hiromi, et al., A 144Mb 8-Level NAND Flash Memory with Optimized Pulse Width Programming, 1999 Symposium on VLSI Circuits Digest of Technical Papers, pp. 39-40	
JA		OHKAWA, Masayoshi, et al., TP 2.3: A 98 mm ² 3.3V 64Mb Flash Memory with FN-NOR Type 4-level Cell, 1996 IEEE International Solid-State Circuits Conference, pp. 36-37	

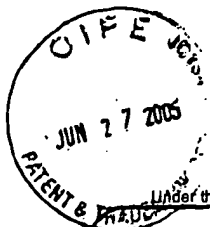
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First Named Inventor	Guterman
Art Unit	2186
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Attorney Docket Number	SAND-01011US0

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U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
JHA		US- 5,926,409	7/20/1999	Engh et al.	
		US-			
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

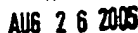
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Filing Date	January 27, 2004
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First Named Inventor	Guterman
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Art Unit	2186
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Examiner Name

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U. S. PATENT DOCUMENTS

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		Country Code ² *Number ⁴ *Kind Code ⁵ (# known)				
		EP 1249842	10/16/2002	STMicroelectronics S.r.l.		

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